

Applicant: TRAININ, Solomon B.
Serial No.: 10/816,846
Filed: April 5, 2004
Page 2

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AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listing of claims in the Application. Please amend the claims to read as follows and cancel without prejudice or disclaimer the claims marked as canceled:

Listing of Claims

- 1.-3. (Canceled)
4. (Currently Amended) The method of Claim ~~[[38]]~~ 66, comprising dividing said associated selected command ~~of said selected interrupt~~ into at least two or more segments, wherein a segment may be processed to completion within the period between consecutive interrupts in said series of periodic interrupts. ~~before said end time.~~
5. (Currently Amended) The method of Claim 4, wherein said ~~associated~~ selected command is a background command.
6. (Withdrawn) A method comprising prohibiting interrupts of a processor during a slot other than interrupts by a command scheduled for processing during said slot.
7. (Withdrawn) The method as in claim 6, comprising scheduling, prior to the start of said slot, said command to be processed by said processor during said slot.
8. (Withdrawn) The method as in claim 7, wherein said scheduling comprises selecting a background command to be processed by said processor during said slot.
9. (Withdrawn) The method as in claim 8, wherein scheduling said background command comprises selecting a load estimation command.
10. (Withdrawn) The method as in claim 8, wherein said selecting said background command comprises selecting a segment of said background command to be processed during said slot.
11. (Withdrawn) The method as in claim 6, comprising selecting said command to be processed by said processor on the basis of a pre-determined priority among commands waiting to be processed by said processor.

Applicant: TRAININ, Solomon B.
Serial No.: 10/816,846
Filed: April 5, 2004
Page 3

12. (Withdrawn) The method as in claim 6, comprising masking a signal of a slot timer during a processing of a transmit command.
13. (Withdrawn) The method as in claim 6, comprising storing in a data storage unit an indication of commands waiting to be processed by said processor.
14. (Withdrawn) The method as in claim 13, comprising storing in a second data storage unit an indication of a slot timer signal that may interrupt said processor during said slot.
15. (Withdrawn) The method as in claim 14, comprising comparing a value stored in a designated position of said data storage unit with a value stored in a designated position of said second data storage unit.
- 16.-18. (Canceled)
19. (Withdrawn) A method comprising delaying during a slot an interruption of a processor.
20. (Withdrawn) The method as in claim 19, comprising scheduling, prior to the start of said slot, a command to be processed by said processor during said slot.
21. (Withdrawn) The method as in claim 19, wherein said delaying comprises delaying an interrupt of said processor during the processing of a background command.
22. (Withdrawn) The method as in claim 19, comprising dividing a background command into segments, said segments capable of being processed to completion within the time available in a slot.
- 23.-28. (Canceled)
29. (Withdrawn) An apparatus comprising:
a host; and
a controller,
said controller to prohibit interrupts of a processor during a slot other than by a command scheduled for processing during said slot.
30. (Withdrawn) The apparatus as in claim 29, wherein said controller is to schedule, prior to the start of said slot, said command to be processed by said processor during said slot.

Applicant: TRAININ, Solomon B.
Serial No.: 10/816,846
Filed: April 5, 2004
Page 4

31. (Withdrawn) The apparatus as in claim 29, wherein said controller is to select a background command to be processed by said processor during said slot.
- 32.-34. (Canceled)
35. (Withdrawn) A device comprising a controller to delay an interrupt of a processor during a slot.
36. (Withdrawn) The device as in claim 35, said controller to schedule, prior to the start of said slot, a command to be processed by said processor during said slot.
37. (Withdrawn) The device as in claim 35, said controller to divide a background command into segments, said segments capable of being processed to completion by said processor within the time available in said slot.
38. (Canceled)
39. (Currently Amended) The method of Claim [[38]] 66, wherein said selected command ~~interrupt is an interrupt associated with~~ is a background command if an ~~interrupt from a slot timer and an interrupt associated with a~~ said background command is waiting to be processed and have been scheduled and an interrupt associated with a transmit command, an interrupt associated with a receive command, an interrupt associated with a time accurate command, and an interrupt associated with a command determined to be more important than ~~[[a]] said background command are not waiting to be processed. have not been scheduled.~~
40. (Currently Amended) The method of Claim [[38]] 66, wherein said selected command is ~~interrupt is an interrupt associated with~~ a command if an ~~interrupt from a slot timer and an interrupt associated with a~~ said command is waiting to be processed and have been scheduled and an interrupt associated with a receive command is not waiting to be processed. has not been scheduled.
41. (Currently Amended) The method of Claim [[38]] 66, wherein said selected command is ~~interrupt is an interrupt associated with~~ a receive command if an ~~interrupt from a slot timer and an interrupt associated with a~~ said receive command is waiting to be processed. have been scheduled.

Applicant: TRAININ, Solomon B.
Serial No.: 10/816,846
Filed: April 5, 2004
Page 5

42. (Currently Amended) The method of Claim [[38]] 66, wherein said selected command is interrupt ~~is an interrupt associated with a time accurate command if an interrupt from a slot timer and an interrupt associated with a said time accurate command is waiting to be processed and have been scheduled and an interrupt associated with a transmit command and an interrupt associated with a receive command are not waiting to be processed, have not been scheduled.~~
43. (Currently Amended) The method of Claim [[38]] 66, wherein said selected command is interrupt ~~is an interrupt associated with a transmit command if an interrupt from a slot timer and an interrupt associated with a said transmit command is waiting to be processed and have been scheduled and an interrupt associated with a receive command is not waiting to be processed, has not been scheduled.~~
44. (Currently Amended) The method of Claim [[38]] 66, wherein said series of periodic interrupts ~~start time and said end time~~ are synchronized to a wireless link event.
45. (Currently Amended) The method of Claim [[38]] 66, wherein said series of periodic interrupts ~~start time and said end time~~ are synchronized to [[a]] the time between frame sequences, ~~a sequence of frames.~~
46. (Canceled)
47. (Currently Amended) The wireless device of Claim [[46]] 67, wherein said selected command interrupt ~~is an interrupt associated with~~ is a background command if ~~an interrupt from a slot timer and an interrupt associated with a said background command is waiting to be processed and have been scheduled and an interrupt associated with a transmit command, an interrupt associated with a receive command, an interrupt associated with a time accurate command, and an interrupt associated with a command determined to be more important than [[a]] said background command are not waiting to be processed, have not been scheduled.~~

Applicant: TRAININ, Solomon B.
Serial No.: 10/816,846
Filed: April 5, 2004
Page 6

48. (Currently Amended) The wireless device of Claim [[46]] 67, wherein said selected command is interrupt ~~is an interrupt associated with a command if an interrupt from a slot timer and an interrupt associated with a said command is waiting to be processed and have been scheduled and an interrupt associated with a receive command is not waiting to be processed.~~ has not been scheduled.
49. (Currently Amended) The wireless device of Claim [[46]] 67, wherein said selected command is interrupt ~~is an interrupt associated with a receive command if an interrupt from a slot timer and an interrupt associated with a said receive command is waiting to be processed.~~ have been scheduled.
50. (Currently Amended) The wireless device of Claim [[46]] 67, wherein said selected command is interrupt ~~is an interrupt associated with a time accurate command if an interrupt from a slot timer and an interrupt associated with a said time accurate command is waiting to be processed and have been scheduled and an interrupt associated with a transmit command and an interrupt associated with a receive command are not waiting to be processed.~~ have not been scheduled.
51. (Currently Amended) The wireless device of Claim [[46]] 67, wherein said selected command is interrupt ~~is an interrupt associated with a transmit command if an interrupt from a slot timer and an interrupt associated with a said transmit command is waiting to be processed and have been scheduled and an interrupt associated with a receive command is not waiting to be processed.~~ has not been scheduled.
52. (Currently Amended) The wireless device of Claim [[46]] 67, wherein said series of periodic interrupts ~~start time and said end time~~ are synchronized to a wireless link event.
53. (Currently Amended) The wireless device of Claim [[46]] 67, wherein said series of periodic interrupts ~~start time and said end time~~ are synchronized to [[a]] the time between frame sequences, a sequence of frames.

Applicant: TRAININ, Solomon B.
Serial No.: 10/816,846
Filed: April 5, 2004
Page 7

54. (Currently Amended) The wireless device of Claim ~~[[46]]~~ 67, wherein said instructions result in dividing said ~~associated selected~~ command ~~of said selected~~ interrupt into at least two or more segments, and wherein a segment may be processed to completion within the period between consecutive interrupts in said series of periodic interrupts. ~~before said end time.~~
55. (Currently Amended) The wireless device of Claim 54, wherein said ~~associated~~ selected command is a background command.
56. (Canceled)
57. (Currently Amended) The wireless device of Claim ~~[[56]]~~ 68, wherein said selected command ~~interrupt is an interrupt associated with~~ is a background command ~~if an interrupt from a slot timer and an interrupt associated with a said~~ background command is waiting to be processed and have been scheduled ~~and an interrupt associated with a transmit command, an interrupt associated with a receive command, an interrupt associated with a time accurate command, and an interrupt associated with a command determined to be more important than [[a]] said~~ background command are not waiting to be processed. ~~have not been scheduled.~~
58. (Currently Amended) The wireless device of Claim ~~[[56]]~~ 68, wherein said selected command is ~~interrupt is an interrupt associated with~~ a command ~~if an interrupt from a slot timer and an interrupt associated with a said~~ command is waiting to be processed and have been scheduled ~~and an interrupt associated with a receive command is not waiting to be processed.~~ ~~has not been scheduled.~~
59. (Currently Amended) The wireless device of Claim ~~[[56]]~~ 68, wherein said selected command is ~~interrupt is an interrupt associated with~~ a receive command ~~if an interrupt from a slot timer and an interrupt associated with a said~~ receive command is waiting to be processed. ~~have been scheduled.~~

Applicant: TRAININ, Solomon B.
Serial No.: 10/816,846
Filed: April 5, 2004
Page 8

60. (Currently Amended) The wireless device of Claim [[56]] 68, wherein said selected command is interrupt ~~is an interrupt associated with a time accurate command if an interrupt from a slot timer and an interrupt associated with a said time accurate command is waiting to be processed and have been scheduled and an interrupt associated with a transmit command and an interrupt associated with a receive command are not waiting to be processed, have not been scheduled.~~
61. (Currently Amended) The wireless device of Claim [[56]] 68, wherein said selected command is interrupt ~~is an interrupt associated with a transmit command if an interrupt from a slot timer and an interrupt associated with a said transmit command is waiting to be processed and have been scheduled and an interrupt associated with a receive command is not waiting to be processed, has not been scheduled.~~
62. (Currently Amended) The wireless device of Claim [[56]] 68, wherein said series of periodic interrupts start time and said end time are synchronized to a wireless link event.
63. (Currently Amended) The wireless device of Claim [[56]] 68, wherein said series of periodic interrupts start time and said end time are synchronized to [[a]] the time between frame sequences, a sequence of frames.
64. (Currently Amended) The wireless device of Claim [[56]] 68, wherein said instructions result in dividing said associated selected command of said selected interrupt into at least two or more segments, and wherein a segment may be processed to completion within the period between consecutive interrupts in said series of periodic interrupts, before said end time.
65. (Currently Amended) The wireless device of Claim 64, wherein said associated selected command is a background command.

Applicant: TRAININ, Solomon B.
Serial No.: 10/816,846
Filed: April 5, 2004
Page 9

66. (New) A method for processing by a processor, comprising:
determining if at least one command is waiting to be processed;
enabling acceptance of an interrupt in a series of periodic interrupts by a timer if said at least one command is waiting to be processed;
disabling acceptance of said interrupt if no command is waiting to be processed;
selecting a command waiting to be processed based on a priority;
disabling acceptance of interrupts by commands other than said selected command;
processing at least a portion of said selected command after acceptance of said interrupt; and
disabling acceptance of interrupts in said series of periodic interrupts until completion of said processing if said processing cannot be completed within the period between consecutive interrupts in said series of periodic interrupts.
67. (New) A wireless device comprising a processor readable storage medium having instructions for a processor stored thereon that, when executed by the processor, result in:
determining if at least one command is waiting to be processed;
enabling acceptance of an interrupt in a series of periodic interrupts by a timer if said at least one command is waiting to be processed;
disabling acceptance of said interrupt if no command is waiting to be processed;
selecting a command waiting to be processed based on a priority;
disabling acceptance of interrupts by commands other than said selected command;
processing at least a portion of said selected command after acceptance of said interrupt; and
disabling acceptance of interrupts in said series of periodic interrupts until completion of said processing if said processing cannot be completed within the period between consecutive interrupts in said series of periodic interrupts.

Applicant: TRAININ, Solomon B.
Serial No.: 10/816,846
Filed: April 5, 2004
Page 10

68. (New) A wireless device, comprising:

- a processor;
- a dipole antenna operably connected to said processor; and
- a processor readable storage medium having instructions for said processor stored thereon that, when executed by said processor, result in:
 - determining if at least one command is waiting to be processed;
 - enabling acceptance of an interrupt in a series of periodic interrupts by a timer if said at least one command is waiting to be processed;
 - disabling acceptance of said interrupt if no command is waiting to be processed;
 - selecting a command waiting to be processed based on a priority;
 - disabling acceptance of interrupts by commands other than said selected command;
 - processing at least a portion of said selected command after acceptance of said interrupt; and
 - disabling acceptance of interrupts in said series of periodic interrupts until completion of said processing if said processing cannot be completed within the period between consecutive interrupts in said series of periodic interrupts.